ABSTRACT

A control technique for the auxiliary buck/boost converter is proposed herein to improve the load transient response of the buck converter. The proposed technique improves the system efficiency by enabling the soft switching operation of the auxiliary converter. The design guidelines for achieving capacitor charge balance for the output capacitor during the transient are also presented herein. The experimental results revealed that the output voltage undershoot and settling time during the load step-up transient were 40 mV and 14 μs, respectively, and the output voltage overshoot and settling time during the load step-down transient were 35 mV and 21 μs, respectively. The performance and effectiveness of the proposed technique were experimentally verified using a prototype buck converter with a 15–V input, 3.3–V output, and 200–kHz switching frequency.

1. Introduction

Various control techniques have previously been developed to improve the load transient response of the buck converter. The time-optimal control based on the capacitor charge balance (CCB) enables minimum output voltage fluctuation without the aid of auxiliary circuits[1]. Previous studies have emphasized the simple implementation and operation of resistive auxiliary circuits[2][3]. The auxiliary switched resistor attenuates the output voltage fluctuation, reduce the efficiency, and increase the electromagnetic interference (EMI) caused by the high di/dt of the switches[2]. The auxiliary inductor and bidirectional switches require complicated control circuits and impose a heavy computational burden on the digital integrated circuit (IC) [3]. Additionally, the resistance in the freewheeling path degrades the efficiency.

Control techniques employing nonresistive auxiliary circuits can also improve the dynamic response[4][5][6]. The auxiliary inductor exploits the resonance and soft switching[4], but the auxiliary switches must be controlled differently and according to whether the load is stepped up or down, which increases the controller complexity. The high-frequency switching controllers of the auxiliary circuit have been described in previous papers[5][6]. The average current of the auxiliary inductor has been controlled to a constant value to achieve CCB during the load transient[5]. Adaptive slope control has also been proposed to further improve the load transient response[6]. However, these control techniques require high-bandwidth sensors and high-speed digital ICs, which increase the controller complexity.

This paper proposed a technique for peak–current–controlling the auxiliary current in critical conduction mode (CRM). The technique minimizes the output voltage deviation during the load transient through cycle–by–cycle CCM, while the soft turn–on of the auxiliary switches suppresses the efficiency degradation and EMI generation. The remainder of this paper is organized as follows: Section 2 describes the circuit operation and design conducted via the proposed control scheme. The experimental results are presented in Section 3, and the conclusions are presented in Section 4.

2. Proposed Control Technique

2.1 Implementation of proposed control circuit

Fig. 1 shows the main converter (Q1, Q2, and L1) and auxiliary buck/boost converter (Q3, Q4, and L2) connected in parallel. The literals V_in, V_o, i_L, i_s, and i_o denote the input voltage, output voltage, main inductor current, auxiliary inductor current, and load current, respectively. The voltages v_s and v_d denote the gate–source and drain–source voltages of the metal–oxide–semiconductor field–effect transistor (MOSFET) Q4, respectively. In the steady state, Q3 and Q4 are turned off, while Q1 and Q2 are operational via the conventional voltage mode controller (VMC). During the load transient, Q3 and Q4 operate along with Q1 and Q2 to control i_L and improve the transient response.

The key waveform of the proposed auxiliary current control is shown in Fig. 2. The literals V_in and Δi_o are the target output voltage and magnitude of the i_o variation, respectively. During the transient period caused by stepping up i_o and t1–t5, Q1 is fully turned on while Q2 is

![Fig. 1. Main buck converter (Q1, Q2, and L1) and auxiliary buck/boost converter (Q3, Q4, and L2) for applying proposed auxiliary current control.](image-url)
fully turned off shown in Fig. 2. The auxiliary converter works as a peak-current-mode-controlled buck converter in the CRM. Switch Q0 operates as the main switch of the auxiliary buck converter to produce the triangular wave shapes of i_{L2} under an envelope. The first switching period of the auxiliary converter comprises three switching states:

- **State 1 \([t_1-t_2]\):** Current i_0 is stepped up at \(t_1\); Q3 turns on, and i_{L2} increases linearly.

- **State 2 \([t_2-t_3]\):** When i_{L2} reaches the envelope at \(t_2\), Q3 turns off, and i_{L2} decreases and freewheels through the body diode of Q5. Switch Q5 can be ON as a synchronous rectifier to reduce the conduction loss.

- **State 3 \([t_3-t_4]\):** When i_{L2} reaches zero at \(t_3\), the output parasitic capacitances of Q5 and Q4 and L_2 resonate. Switch Q3 turns on again at \(t_4\), when \(v_{ds3}\) is minimum. During this state, the \(v_o\) variation is assumed to be negligible.

These states are repeated N times before the steady-state operation resumes and VMC regains control at \(t_5\). The number N can be limited to avoid the risk of excessively high switching frequencies and power losses. The switching loss of Q3 is minimized via valley switching, that is, by Q5 turning on at the valley of \(v_{ds3}\). Switch Q3 may turn on with zero-voltage switching (ZVS) if \(V_m \leq 2V_o\).

Area \(A_6\) represents the amount of charge that is charged to or discharged from the output capacitor \(C_o\), to which the \(v_o\) deviation is proportional. The coefficient \(k_o\) is set to maintain the CCB of \(C_o\) in each switching cycle, as expressed in (1).

\[
A_1 + A_{i,2} = A_{i+1} \quad (i=1,4,7,\ldots)
\]  

(1)

Section 2.2 presents the derivation of \(k_o\) in detail. The undershoot of \(v_o\) and \(\Delta v_0\) is the function of voltages, inductances, and \(\Delta I_o\) and is solely determined by \(A_1\), as expressed in (2).

\[
\Delta v_o = \frac{A_1}{C_o} = \frac{\Delta I_o^2 L_2}{2O(C_oL_2+L_2)V_{in}-V_o)
\]

(2)

When \(i_0\) is stepped down, the auxiliary converter operates as a peak-current-mode-controlled boost converter in CRM. Switch Q4 acts as the main switch of the auxiliary boost converter and turns on with ZVS if \(V_m > 2V_o\), while Q1 is fully on and Q2 is fully on.

### 2.2 Envelope for Capacitor-Charge-Balance

The magnitude of i_{L2} is controlled by the envelope of which height is \((k_o+1)\Delta I_o\) as shown in Fig. 2. The constant \(k_o\) is a function of \(V_{in}, V_o, L_1,\) and \(L_2\) and is independent from \(\Delta I_o\), as expressed in (3).

\[
k_o = \frac{L_1(V_m-V_o)+L_2(V_m-2V_o)}{L_1(V_m-V_o)+L_2(V_m-2V_o)}
\]

(3)

The other constant \(k_o\) that determines the envelope when \(i_0\) is stepped down can be obtained using (4).

\[
k_o = \frac{L_1(V_m-V_o)}{L_1(V_m-V_o)+L_2(V_m-2V_o)}
\]

(4)

In practical implementation, the first turn-on of Q3 may be delayed by the time \(\Delta t_o\) from the step change of \(i_0\). Fig. 3 plots \(k_o\) and \(k_o\) versus \(v_o\) when \(V_{in}, L_1, L_2,\) and \(I_0\) are as presented in Table 1. The plots are symmetric with respect to the centroid.
to the $V_0 = V_d/2$ line (7.5 V in this paper) when $\Delta t_d = 0$, because the increasing and decreasing slope of $i_{L1} + i_{L2}$ are interchanged between the step-up and step-down cases of $i_o$. The nonzero $\Delta t_d$ increases both $k_d$ and $k_u$.

3. Experimental Verification

The prototype circuit shown in Fig. 1 was built: its parameters are listed in Table 1. The VMC control loop had a $78.6^\circ$ phase margin at the 12-kHz cutoff frequency via a PIC type-3 compensator and the control circuit was configured by analog ICs only. Fig. 4 shows the experimental waveform of $i_o$ (magenta trace), $i_{L1} + i_{L2}$ (red), $v_o$ (blue), $v_{ds3}$ and $v_{ds4}$ (green) of the proposed control scheme when $i_o$ was stepped up and down, respectively. The delay $\Delta t_d$ was 1.2 $\mu$s at step-up and 0.5 $\mu$s at step-down. In Fig. 4(a), The measured undershoot voltage and settling time were 40 mV and 14 $\mu$s, respectively. As can be seen, $Q_3$ valley-switched to reduce the switching loss and noise. In Fig. 4(b), the measured overshoot voltage and settling time were 35 mV and 21 $\mu$s, respectively. The $Q_1$ was zero-voltage-switched, reducing switching loss and noise during the load step-down transient.

4. Conclusion

An auxiliary current control technique is proposed herein to improve the load transient response of buck converter. The proposed scheme controls the auxiliary buck/boost converter through the peak current mode in CRM. The proposed control scheme minimizes the output voltage deviation and power loss of the auxiliary circuit via soft-switching auxiliary switches. Experimental results by 15 V–3.3 V, 200–kHz buck converter demonstrated the improved dynamic response: the undershoot was 40 mV and the settling time was 14 $\mu$s when the output current was stepped from 4 A to 15 A, and the overshoot was 35 mV and settling time was 21 $\mu$s when the output current was stepped from 15 A to 4 A.

References